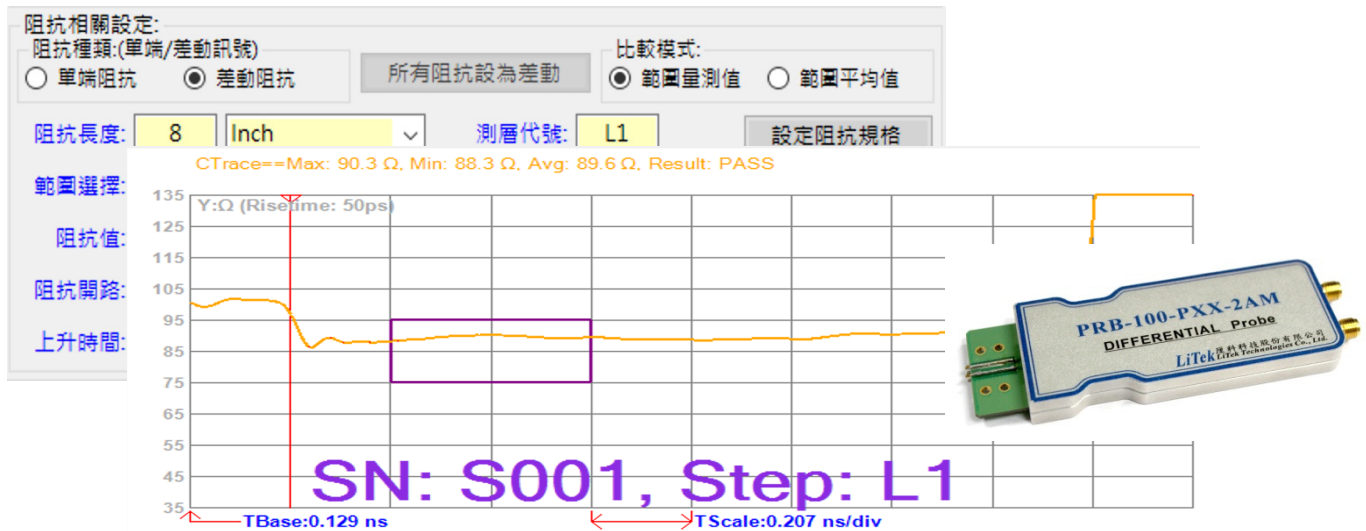


PCB IMPEDANCE Test Solution



❖ Introduce

LiTek Technologies develop a total solution in PCB Impedance Measurement. The LiTek's ENAPro software with The Keysight Network Analyzer can test the PCB Impedance quickly. The test system can support general test probe ,and the user also can use LiTek's test probe which can get the best test result.

When user Build a new project, that can set the sample quantity and how many trace you want to test of the sample. LiTek's ENAPro software also support sample added capability when the user want to test more sample in measurement.

Base on the user requirement, LiTek's ENAPro provide many setting in measurement, such as: Type of Impedance, Compare Type, The length of DUT, Layer name, Impedance SPEC.

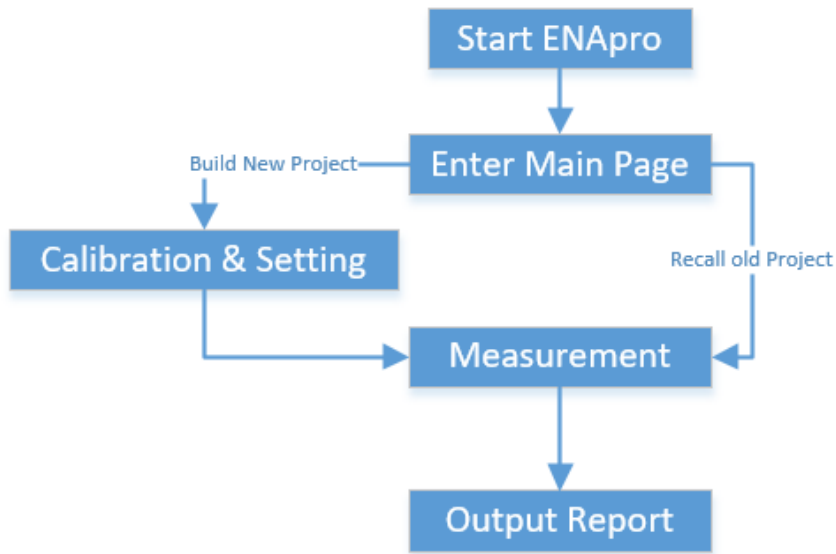
❖ Feature

- Sample by Sample or Step by Step measurement.
- Sample added capable in measurement.
- PCB Range selected by “Time” , “Length” or “Open to Open” .

- Single-ended and Differential Impedance in one project.
- Compare by Impedance Average or max/min in selected range.
- Rise-time setup for different request. (50ps, 20~80% for example)
- One Calibration for all project and Re-Calibration remind.
- Foot Switch button supported.
- Auto Open to Open position found by ENAPro.
- Multi Traces displayed in one step measurement.
- All samples of one step impedance traces displayed.
- All samples average spread of selected step displayed.
- Report with step to step or sample to sample and PCB impedance traces for option
- Samples output selection (decide which samples are output).
- *.txt (data only) and *.xls (data and graphics) format supported.
- External PC or VNA Embedded software environment.

PCB IMPEDANCE Test Solution

✧ PCB Impedance Measurement Flow Chart



✧ Report format

報表順序: 所有樣品同一阻抗完成後,換下一個所有樣品阻抗

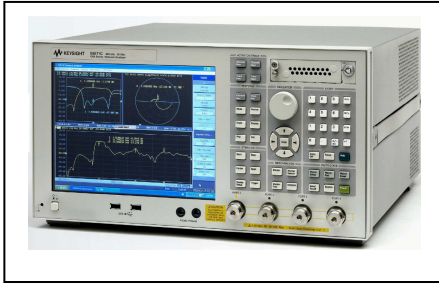
輸出: 同一樣品內所有阻抗完成後換下一個樣品

所有樣品同一阻抗完成後,換下一個所有樣品阻抗

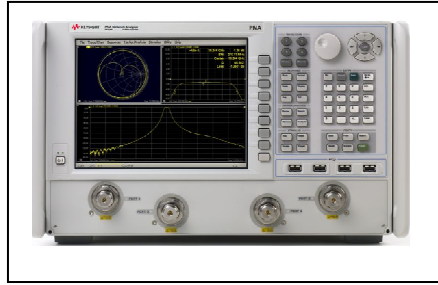
Sample3 Result:							L1 Result:										
Result	Sample	Step Imp	Spc Max	Spc Min	Mt		Result	Sample	Step Imp	Spc Max	Spc Min	Max Val	Min Val	Avg Val	Delta Val	Stdev	Date/Time
PASS	S003	L1	95	75	9		PASS	S001	L1	95	75	94.2	91.8	92.7	2.5	0.717	2017-09-16 14:45
PASS	S003	L2	95	75	8		PASS	S002	L1	95	75	92.2	89.7	90.6	2.5	0.721	2017-09-16 14:45
							PASS	S003	L1	95	75	92.2	89.7	90.6	2.5	0.728	2017-09-16 14:45
							PASS	S004	L1	95	75	92.1	89.7	90.6	2.4	0.721	2017-09-16 14:45
							PASS	S005	L1	95	75	92.1	89.6	90.5	2.5	0.724	2017-09-16 14:45
							PASS	S006	L1	95	75	92.2	89.8	90.7	2.4	0.717	2017-09-16 14:45
							PASS	S007	L1	95	75	92.3	89.8	90.7	2.5	0.724	2017-09-16 14:45
							PASS	S008	L1	95	75	92	89.6	90.5	2.5	0.722	2017-09-16 14:46
							PASS	S009	L1	95	75	92.1	89.6	90.5	2.5	0.723	2017-09-16 14:46
							PASS	S010	L1	95	75	92.1	89.6	90.5	2.5	0.721	2017-09-16 14:46
							PASS	S011	L1	95	75	92	89.6	90.5	2.5	0.727	2017-09-16 14:46
							PASS	S012	L1	95	75	92	89.5	90.4	2.5	0.727	2017-09-16 14:46
							PASS	S013	L1	95	75	92.1	89.6	90.5	2.5	0.723	2017-09-16 14:46
							PASS	S014	L1	95	75	92	89.6	90.5	2.5	0.723	2017-09-16 14:46
							PASS	S015	L1	95	75	92	89.6	90.5	2.5	0.721	2017-09-16 14:47
							PASS	S016	L1	95	75	92	89.5	90.4	2.5	0.72	2017-09-16 14:47
							PASS	S017	L1	95	75	92	89.6	90.5	2.5	0.724	2017-09-16 14:47
							PASS	S018	L1	95	75	92.1	89.6	90.5	2.5	0.721	2017-09-16 14:47
							PASS	S019	L1	95	75	92.1	89.6	90.5	2.5	0.722	2017-09-16 14:47
							PASS	S020	L1	95	75	92	89.5	90.4	2.5	0.723	2017-09-16 14:47
							PASS	S021	L1	95	75	92.1	89.6	90.5	2.5	0.723	2017-09-16 14:47
							PASS	S022	L1	95	75	92	89.6	90.5	2.5	0.72	2017-09-16 14:47
							PASS	S023	L1	95	75	92	89.5	90.4	2.5	0.721	2017-09-16 14:47
							PASS	S024	L1	95	75	92.1	89.6	90.5	2.5	0.719	2017-09-16 14:48

PCB IMPEDANCE Test Solution

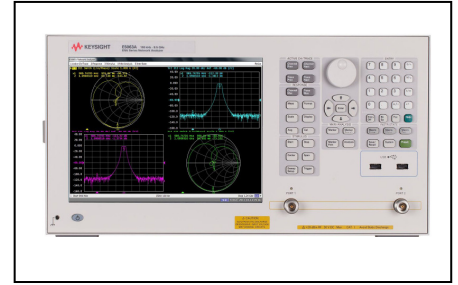
✧ Keysight Equipment



(E5071C)



(N5224A)



(E5063A)

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